

# **Dynamic Expression Trees**

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## Abstract

We present a technique for dynamically maintaining a collection of arithmetic expressions represented by binary trees (whose leaves are variables and whose internal nodes are operators). A query operation asks for the value of an expression (associated with the root of a tree). Update operations include changing the value of a variable and combining or decomposing expressions by linking or cutting the corresponding trees. Our dynamic data structure uses linear space and supports queries and updates in logarithmic time. An important application is the dynamic maintenance of maximum flow and shortest path in series-parallel digraphs under a sequence of vertex and edge insertions, series and parallel compositions, and their respective inverses. Queries include reporting the maximum flow or shortest *st*-path in a series-parallel subgraph.

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# 1 Introduction

The evaluation of tree-structured expressions is a fundamental computational task. In this paper we study the dynamic maintenance of expressions over a semiring  $\mathcal{S}$  with binary operators  $\oplus$  and  $\otimes$ , such that  $\otimes$  is distributive with respect to  $\oplus$ . We also allow the inverse operators of  $\oplus$  and  $\otimes$ , whenever they exist. An expression over  $\mathcal{S}$  is naturally represented by a binary tree  $\mathcal{T}$  where the internal nodes represent operators and the leaves represent variables. The important classes of arithmetic and boolean expressions are special cases of this general model.

We consider the problem of dynamically maintaining a collection of expressions of total size  $n$ . A query operation asks for the value of an expression (or a subexpression associated with a subtree). The repertory of dynamic operations consists of: (a) changing the value of a variable; (b) combining two expressions by linking the corresponding trees; and (c) decomposing an expression into two parts by cutting its tree. In Section 2 we present an efficient solution to this problem by introducing a new data structure, called *dynamic expression tree*, that uses  $O(n)$  space and supports each query or update operation in  $O(\log n)$  time.

Incremental graph evaluation has been extensively studied in the area of programming languages. However, the most advanced techniques previously developed, when applied to our problem, still yield  $O(n)$  worst-case time per update operation (see, e.g., [2,21]).

Our dynamic expression trees are inspired by the dynamic trees of Sleator and Tarjan [23] and the expression restructuring scheme of Brent [6,7]. We show how to combine these classical techniques to yield a powerful and versatile dynamic data structure.

Related work has been focused on the use of parallelism in expression evaluation (see [18]). The most efficient parallel “tree-contraction” algorithms run in time  $O(\log n)$  using an EREW PRAM with  $n/\log n$  processors.

An important application of dynamic expression trees is the dynamic maintenance of the maximum flow and shortest path in series-parallel digraphs. Such digraphs arise in a variety of problems such as scheduling [19], electrical networks (see, e.g., [30]), data-flow analysis [8], database logic programs [1], and circuit layout [25]. Also, some problems that are NP-complete for general graphs admit linear-time solutions when restricted to series-parallel digraphs [26].

We define an extensive repertory of update operations for series-parallel digraphs, which includes insertion of vertices and edges, series and parallel composition of two series-parallel digraphs, and their respective inverses. Query operations include finding the value of a maximum flow (or the length of a shortest source-to-sink path), and determining whether an edge is on some minimum cut (or shortest source-to-sink path). In a static environment maximum flow and shortest source-to-sink path in a series-parallel digraph  $G$  can be computed in  $O(m)$  time, where  $m$  is the current number of edges of  $G$ . In Section 3 we present an  $O(m)$  space dynamic data structure based on dynamic expression trees that supports each query or update operation in time  $O(\log m)$ .

Maximum flow and shortest path are fundamental problems in network optimization and have been extensively investigated [9,14]. A long standing question has been the development of efficient dynamic algorithms for such problems.

The dynamic data structures for all-pairs shortest paths presented in [12,22] have  $O(n^2)$  space,  $O(1)$  query time,  $O(n^2)$  time for edge insertion, and  $O(mn + n^2 \log n)$  time for edge

deletion, where  $n$  is the number of vertices and  $m$  is the number of edges. Such performance bounds, especially for deletion, are quite unsatisfactory and indicate the difficulty of the problem (for lower bound arguments, see [5,24]). The best known semi-dynamic data structure supporting insertions in digraphs with unit edge lengths uses  $O(n^2)$  space and has  $O(1)$  query time; the total time to process all edge insertions is  $O(n^3 \log n)$ , which amortizes to  $O(n \log n)$  time per insertion for dense digraphs [3,20].

Regarding maximum flow, previous results are confined to the *parametric maximum flow problem*, where the capacities of the edges are functions of a parameter  $\lambda$ . In [13,16] it is shown that the value of a maximum flow for  $O(n)$  values of  $\lambda$  can be computed on-line with the same time complexity as the fastest maximum flow algorithm. No efficient algorithms are known for dynamic or semi-dynamic flow problems where update operations change arbitrarily edge capacities or insert/delete edges. Hence, our results give the first efficient technique for maintaining a maximum flow, although limited to the class of series-parallel digraphs.

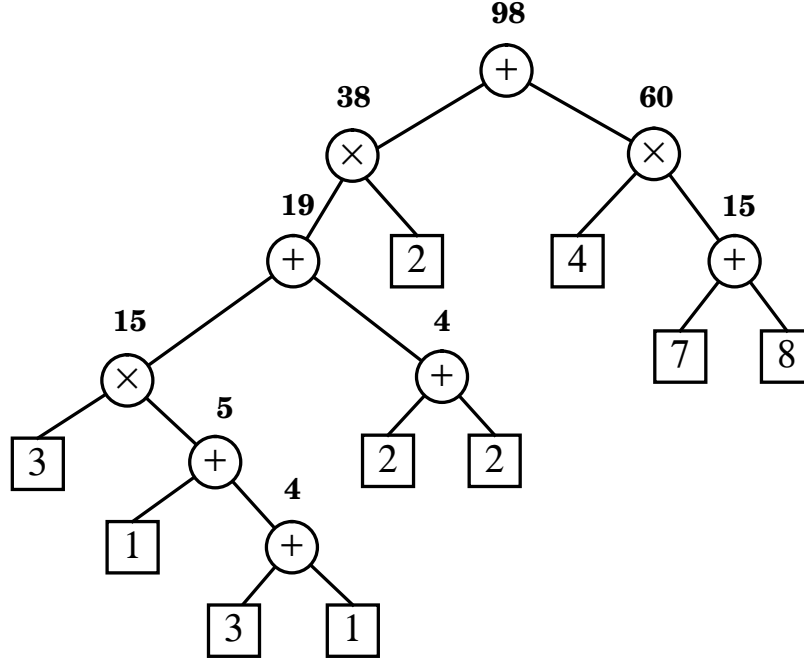
Further applications of dynamic expression trees to constructive solid geometry and layout compaction are given in Section 4. Finally, open problems are discussed in Section 5.

## 2 Dynamic Expression Trees

Let  $\mathcal{S}$  be a semiring with binary operators  $\oplus$  and  $\otimes$  such that  $\otimes$  is distributive with respect to  $\oplus$ . For simplicity, we assume that variables can be stored in  $O(1)$  space and binary operations can be performed in  $O(1)$  time. The extension to the general case is straightforward. An expression  $\mathcal{E}$  over  $\mathcal{S}$  is represented by a binary tree  $\mathcal{T}$ , whose internal nodes are the operators and whose leaves are the variables of  $\mathcal{E}$  (see Fig. 1).

Our dynamic environment supports the following operations on a collection of expressions (see Fig. 2).

- *Evaluate*(node  $\mu$ ) — Return the value of the subexpression associated with the subtree rooted at  $\mu$ .
- *Change*(node  $\lambda$ ; value  $x$ ) — This operation assumes that  $\lambda$  is a leaf. Set the value of the variable associated with  $\lambda$  equal to  $x$ .
- *MakeVar*(value  $x$ ) — Create a new tree consisting of a single leaf whose variable has value  $x$ .
- *DeleteVar*(node  $\lambda$ ) — Remove the single-node tree consisting of leaf  $\lambda$ .
- *Construct*(operator  $\odot$ ; node  $\rho', \rho''$ ) — This operation assumes that  $\rho'$  and  $\rho''$  are the roots of two trees  $\mathcal{T}'$  and  $\mathcal{T}''$ , respectively. Create a new tree  $\mathcal{T}$  whose root  $\rho$  stores operator  $\odot$ , and with left subtree  $\mathcal{T}'$  and right subtree  $\mathcal{T}''$ .
- *Destruct*(node  $\rho$ ) — This operation assumes that  $\rho$  is the root of a tree  $\mathcal{T}$  and is not a leaf. Remove node  $\rho$ , which separates  $\mathcal{T}$  into trees  $\mathcal{T}'$  and  $\mathcal{T}''$  given by the former left and right subtrees of  $\mathcal{T}$ , respectively.
- *Graft*(node  $\rho, \lambda$ ) — This operation assumes that  $\lambda$  is a leaf of a tree  $\mathcal{T}$ , and  $\rho$  is the root of a tree  $\mathcal{T}'$ . Replace  $\lambda$  with  $\rho$ , so that  $\mathcal{T}'$  becomes a subtree of  $\mathcal{T}$ .
- *Prune*(node  $\mu$ ) — Let  $\mathcal{T}$  be the tree containing node  $\mu$ . Detach from  $\mathcal{T}$  the subtree  $\mathcal{T}'$



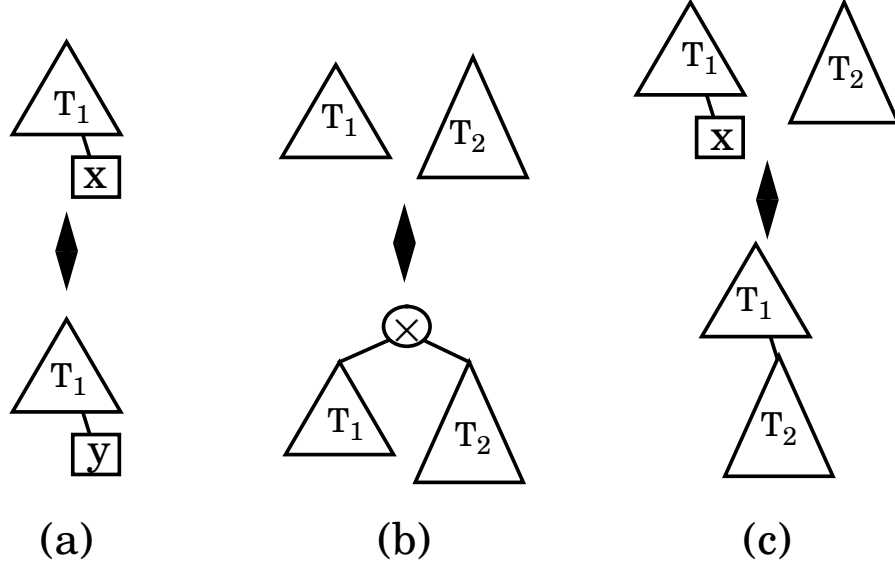
**Figure 1:** The tree for the arithmetic expression  $((((3 \times (1 + (3 + 1))) + (2 + 2)) \times 2) + (4 \times (7 + 8)))$  with ordinary  $+$  and  $\times$  operators. Next to each node is shown the value of the corresponding subexpression.

rooted at  $\mu$  and replace it with a leaf with value  $Evaluate(\mu)$ .

The dependence of the value  $y$  of an expression  $\mathcal{E}$  from the value of variable  $x$  can be expressed in the canonical form  $y = (a \otimes x) \oplus b$ , where  $a$  and  $b$  are elements of  $\mathcal{S}$  [6]. In terms of the tree  $\mathcal{T}$  representing  $\mathcal{E}$ , this property extends immediately to the dependence of the value  $y$  of a node  $\nu$  from the value  $x$  of one of its descendants  $\mu$ . Hence, we can assign the pair  $(a, b)$  to the path of  $\mathcal{T}$  from  $\mu$  to  $\nu$ . We show later (in the description of the *join* operation) how to find and maintain these values for a path between such nodes  $\mu$  and  $\nu$ .

We keep an expression-tree  $\mathcal{T}$  in a data structure based on dynamic trees [23]. Edges of  $\mathcal{T}$  are considered to be directed from the child to the parent. An edge  $(\mu, \nu)$  of  $\mathcal{T}$  is said to be *solid* if the subtree rooted at node  $\mu$  has more than half of the nodes of the subtree rooted at its parent  $\nu$ . Otherwise, edge  $(\mu, \nu)$  is said to be *dashed*. Therefore, there is at most one solid edge entering any node (from its children) and every node is in exactly one maximal path of solid edges (of length 0 or more). We refer to these paths as *solid paths* (see Fig. 3). A solid path  $\Pi$  is represented as a balanced binary tree  $\mathcal{B}_\Pi$  (see Fig. 4), so that  $\mathcal{T}$  is then stored as a collection of these *path trees*. Note that any leaf-to root path in  $\mathcal{T}$  contains  $O(\log n)$  dashed edges, where  $n$  is the size of  $\mathcal{T}$ .

We maintain the invariant, called the *path invariant*, that the tail  $\tau$  and head  $\sigma$  of each solid path store the actual values  $Evaluate(\tau)$  and  $Evaluate(\sigma)$  of their subexpressions. The value of  $Evaluate(\sigma)$  is known if  $\sigma$  is a leaf of  $\mathcal{T}$ , or is calculated from the values of  $Evaluate(\sigma_1)$  and  $Evaluate(\sigma_2)$  for the children  $\sigma_1$  and  $\sigma_2$  of  $\sigma$ . These values will be known by the path invariant, since  $\sigma_1$  and  $\sigma_2$  are the tails of their respective solid paths. We find



**Figure 2:** (a) Operation *Change*. (b) Operations *Construct* and *Destruct*. (c) Operations *Graft* and *Prune*.

the value of  $Evaluate(\tau)$  using the path trees.

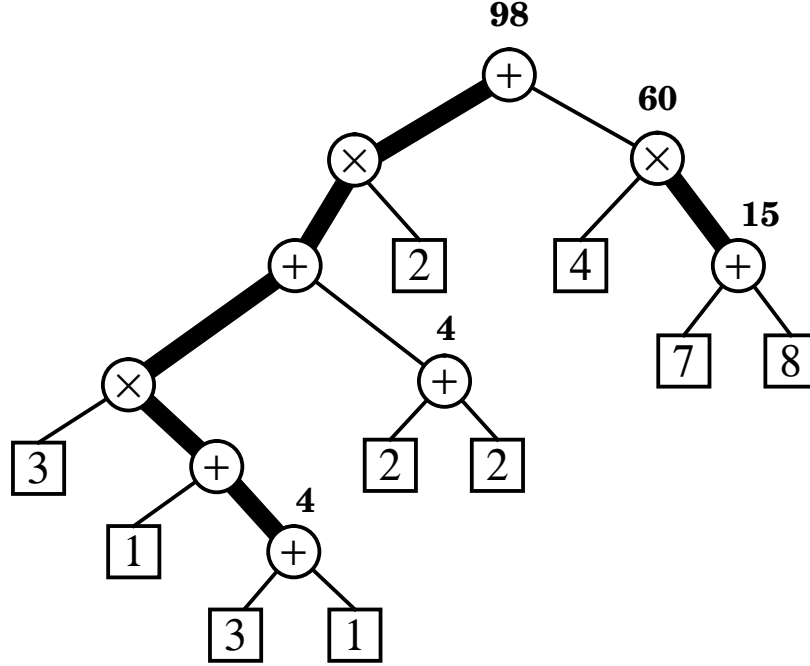
Let  $\Pi$  be a solid path from  $\sigma$  to  $\tau$ . The leaves of tree  $\mathcal{B}_\Pi$  are the nodes of  $\Pi$  in left-to-right order from  $\sigma$  to  $\tau$ . Each internal node  $\eta \in \mathcal{B}_\Pi$  represents the subpath of  $\Pi$  between its external descendants  $head(\eta)$  and  $tail(\eta)$ . We store at  $\eta$  the pair  $(a, b)$  such that (see Fig. 4):

$$Evaluate(tail(\eta)) = (a \otimes Evaluate(head(\eta))) \oplus b.$$

Now, we show how to perform operation  $Evaluate(\mu)$  (see Fig. 5). Let  $\Pi$  be the solid path from  $\sigma$  to  $\tau$  containing  $\mu$ . If  $\mu = \sigma$  or  $\mu = \tau$ , by the path invariant  $\mu$  stores its actual value. Otherwise, the path from  $\sigma$  to  $\mu$  can be decomposed into  $O(\log d)$  subpaths each associated with a node of  $\mathcal{B}_\Pi$ , where  $d$  is the distance between  $\sigma$  and  $\mu$  in  $\mathcal{T}$ . Since  $\sigma$  is the head of the first subpath, its value is known. The value of the tail of each subpath is computed from the value of the head using its  $(a, b)$  pair. The value of the head of the next subpath is directly calculated from the value of its children, which are the tail of the previous subpath and the tail of another solid path. Hence, since the values stored at  $\sigma$  and at the siblings of the nodes of  $\Pi$  are actual, we can compute the value of  $\mu$  in time  $O(\log d)$ .

Operation  $Change(\lambda, x)$  affects only the values of the nodes in the path from  $\lambda$  to the root  $\rho$ . If such path contains dashed edges, then we temporarily convert it into a solid path by changing dashed edges to solid and solid edges to dashed such that any node continues to have at most one incoming solid edge. This is done with the following operations, derived from dynamic trees [23]:

- *splice(path  $\Pi$ )* — This operation assumes that  $\Pi$  is a solid path ending at  $\mu \neq \rho$ . Convert the dashed edge leaving  $\mu$  to solid and convert the solid edge (if it exists) entering the parent  $\nu$  of  $\mu$  to dashed. Let  $\Pi'$  be the solid path containing  $\nu$ . To



**Figure 3:** Solid paths for the expression tree of Fig. 1. The values of the first and last node of each solid path are shown.

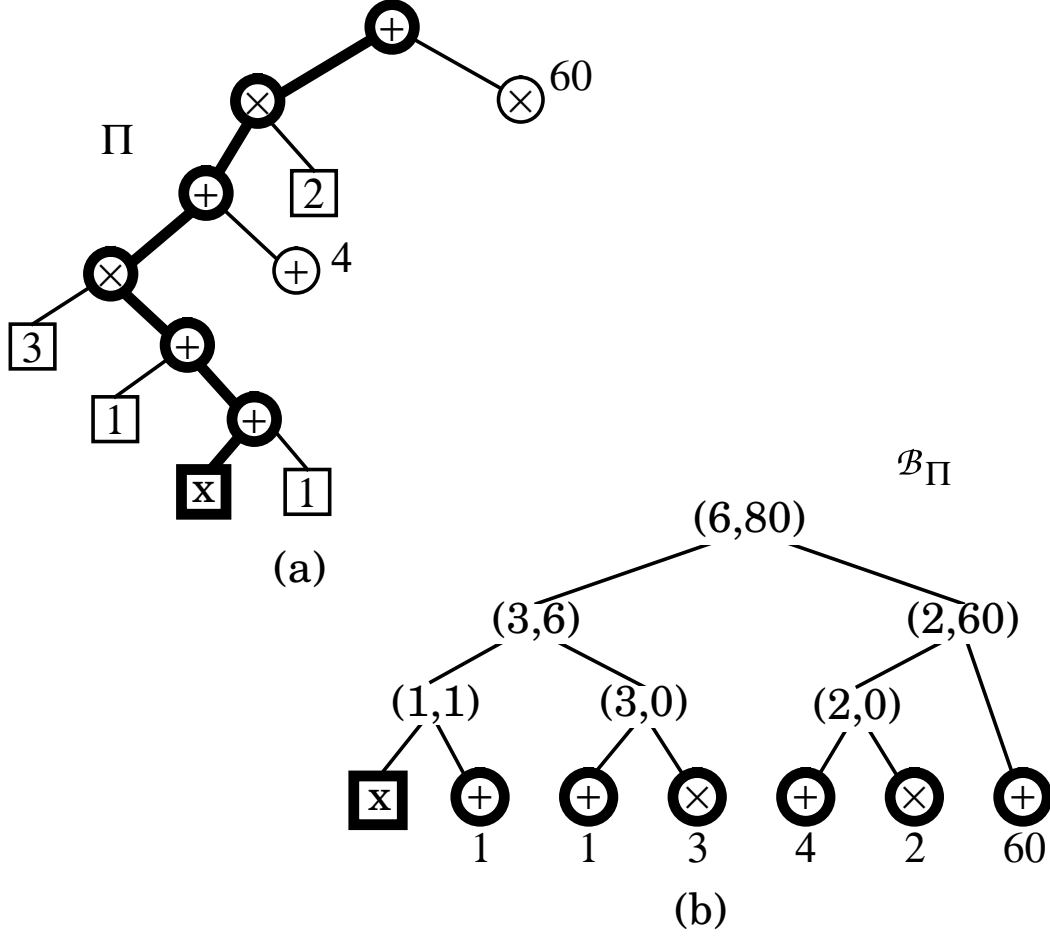
implement  $splice(\Pi)$  we need to convert edge  $(sib(\mu), \nu)$  from solid to dashed ( $sib(\mu)$  denotes the sibling of node  $\mu$ ). Let  $\Pi''$  be the resulting solid path starting at  $\nu$ . We obtain  $\Pi''$  by splitting  $\mathcal{B}_{\Pi'}$ . To maintain the path invariant, we call  $Evaluate(sib(\mu))$  and  $Evaluate(\nu)$ . We complete  $splice$  by concatenating  $\mathcal{B}_{\Pi}$  and  $\mathcal{B}_{\Pi''}$ .

- $expose(\mu)$  — Convert to dashed the solid edge entering  $\mu$ , if such edge exists. Create a solid path from  $\mu$  to the root by converting to solid all the dashed edges  $(\nu', \nu'')$  of such path, and converting to dashed the edges  $(sib(\nu'), \nu'')$ . Operation  $expose(\lambda)$  consists of a sequence of  $splice$  operations on the solid paths containing the nodes on the path from  $\lambda$  to  $\rho$ . This operation is always followed by a  $conceal$  operation, which undoes its effect. An example of  $expose$  operation on the tree of Fig. 1 is shown in Fig. 4.a.
- $conceal(\mu)$  — Restore the original type (solid or dashed) of the edges entering the nodes on the path from  $\mu$  to  $\rho$ . This operation is the inverse of  $expose$ , and also consists of a sequence of  $splice$  operations.

To implement concatenations and splits of the balanced binary trees representing solid paths, we use the following elementary tree operations, each taking  $O(1)$  time:

- $join(node \ \zeta', \zeta'')$  — Given the roots  $\zeta'$  and  $\zeta''$  of two binary trees representing solid paths  $\Pi'$  and  $\Pi''$ , combine the trees into a new tree by creating a new root  $\zeta$  with left child  $\zeta'$  and right child  $\zeta''$ . Let  $\sigma'$  and  $\tau'$  be the head and tail of  $\Pi'$  and  $\sigma''$  and  $\tau''$  be the head and tail of  $\Pi''$ . The  $(a, b)$  pair of  $\zeta$  is computed from the corresponding pairs  $(a', b')$  of  $\zeta'$  and  $(a'', b'')$  of  $\zeta''$ , the operator  $\odot$  on the head node of  $\Pi''$ , and the value  $c$  of the sibling of the tail node of  $\Pi'$  in the following manner:

$$Evaluate(\tau') = a' \otimes Evaluate(\sigma') \oplus b'$$



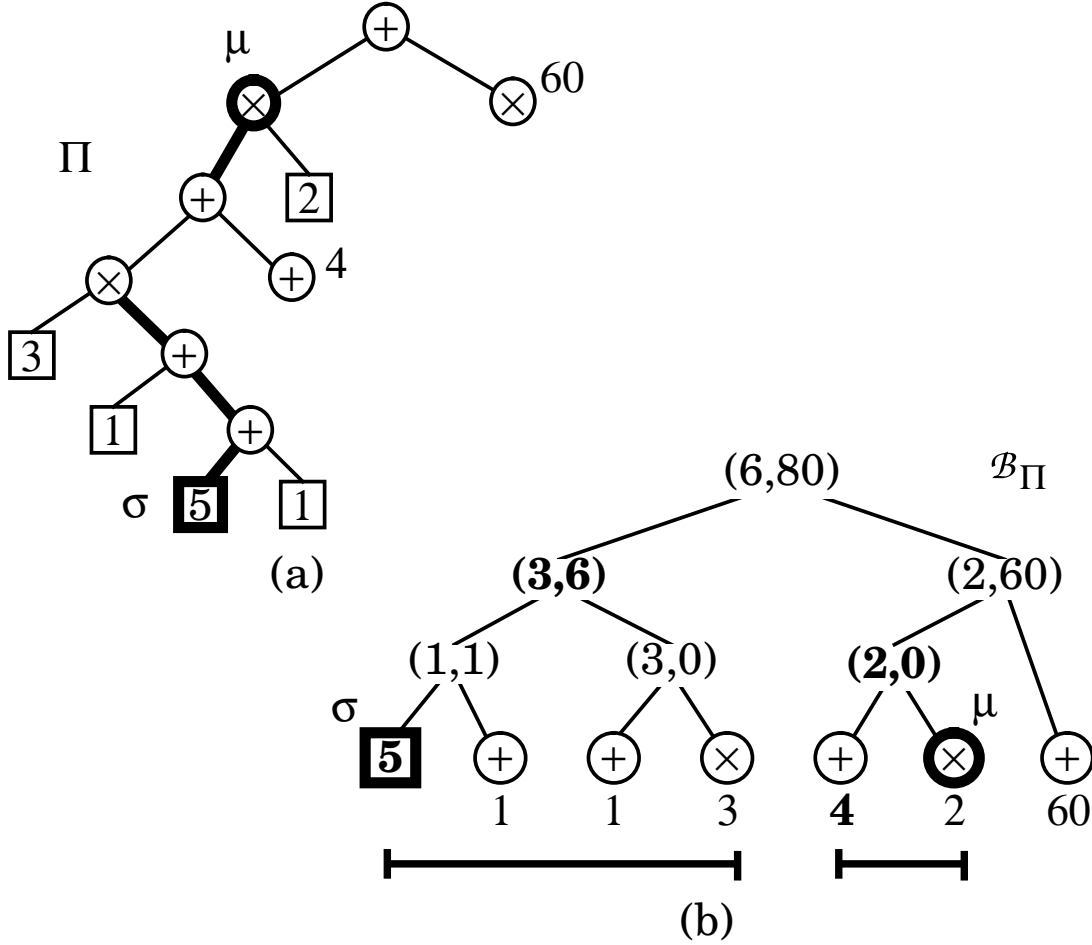
**Figure 4:** (a) A solid path  $\Pi$  created by an *expose* operation in the tree  $\mathcal{T}$  of Fig. 1. (b) The path tree  $\mathcal{B}_\Pi$  for the solid path  $\Pi$ . The leaves of  $\mathcal{B}_\Pi$  represent the nodes of  $\Pi$ . Shown at each leaf is the operator at the corresponding node of  $\Pi$ , and the value of the child of such node that is not on  $\Pi$ . Shown at each internal node  $\eta$  of  $\mathcal{B}_\Pi$  is the  $(a, b)$  pair for the subpath of  $\Pi$  represented by  $\eta$ . For example, the value of the tail of  $\Pi$  in terms of the value  $x$  of the head of  $\Pi$  is  $6 \cdot x + 80$ .

$$\begin{aligned}
\text{Evaluate}(\sigma'') &= \text{Evaluate}(\tau') \odot c \\
\text{Evaluate}(\tau'') &= a'' \otimes \text{Evaluate}(\sigma'') \oplus b'' \\
\text{Evaluate}(\tau'') &= a'' \otimes (\text{Evaluate}(\tau') \odot c) \oplus b'' \\
\text{Evaluate}(\tau'') &= a'' \otimes ((a' \otimes \text{Evaluate}(\sigma') \oplus b') \odot c) \oplus b''
\end{aligned}$$

Therefore, we have:

$$\begin{aligned}
a &= \begin{cases} a' \otimes a'' & \text{if } \odot = \oplus \\ a' \otimes a'' \otimes c & \text{if } \odot = \otimes \end{cases} \\
b &= \begin{cases} (a'' \otimes (b' \oplus c)) \oplus b'' & \text{if } \odot = \oplus \\ (a'' \otimes b' \otimes c) \oplus b'' & \text{if } \odot = \otimes \end{cases}
\end{aligned}$$

An example of operation *join* is shown in Fig. 6.

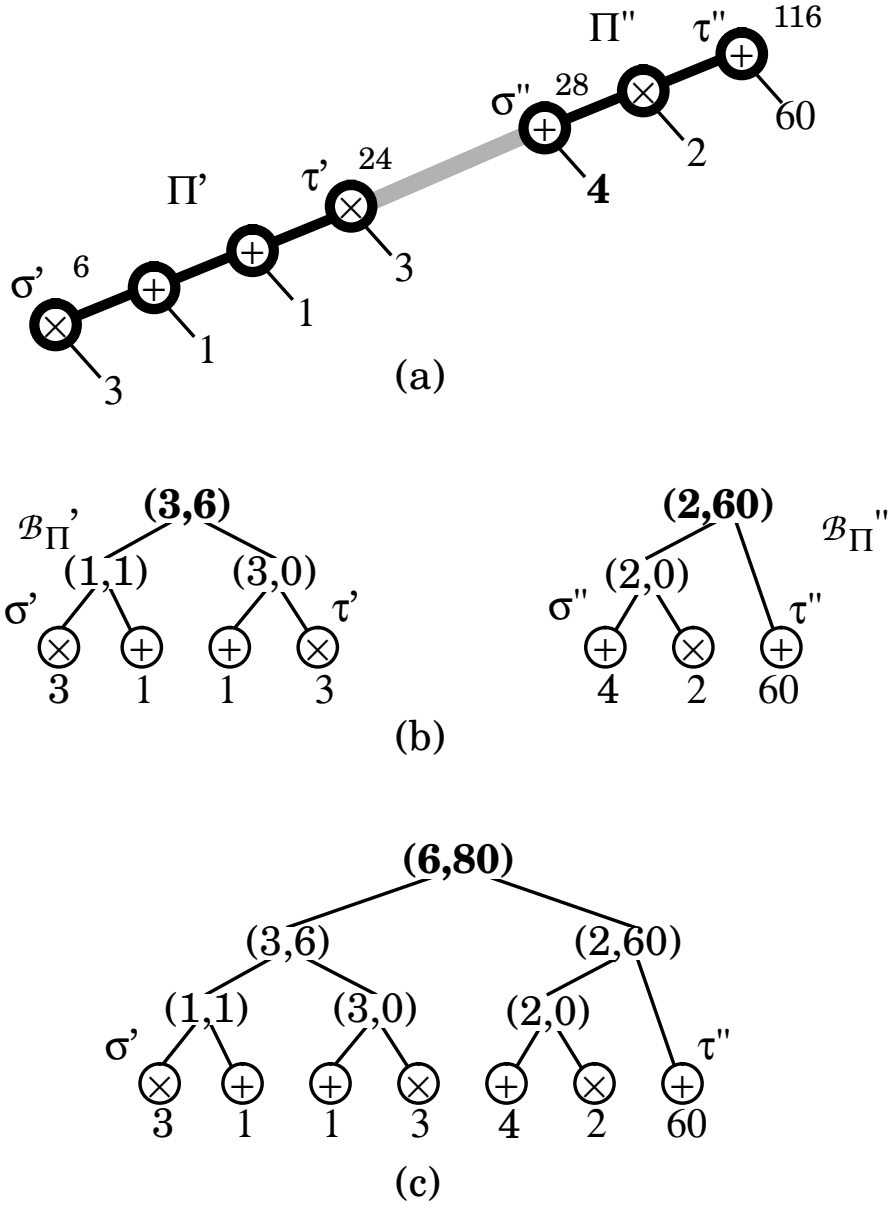


**Figure 5:** (a) A solid path  $\Pi$  with head node  $\sigma$  and an intermediate node  $\mu$ . (b) Computation of the value of  $\mu$  using the path tree  $\mathcal{B}_\Pi$ . The path from  $\sigma$  to  $\mu$  is decomposed into two subpaths so that the value of  $\mu$  is given by  $Evaluate(\mu) = 2 \cdot (4 + (3 \cdot Evaluate(\sigma) + 6)) = 50$ .

- *separate*(node  $\zeta$ ) — Given the root  $\zeta$  of a binary tree, divide the tree into two trees with roots  $\zeta'$  and  $\zeta''$ , where  $\zeta'$  is the root of the left subtree and  $\zeta''$  is the root of the right subtree. The  $(a, b)$  pairs of  $\zeta'$  and  $\zeta''$  do not change.
- *rotateleft*(node  $\eta$ ) (*rotateright*(node  $\eta$ )) — Perform a left (right) rotation at node  $\eta$ . This operation can be performed with  $O(1)$  *separate* and *join* operations.

Operation *Change*( $\lambda, x$ ) is realized as follows. First, we issue *expose*( $\lambda$ ). Next, we change the value of  $\lambda$  to  $x$  and re-evaluate the expression with  $Evaluate(\rho)$ , where  $\rho$  is the root of the tree containing  $\lambda$ . Finally, we perform *conceal*( $\lambda$ ) to restore the original solid and dashed edges. We use biased search trees [4] to represent the path-trees, where the weight of node  $\mu$  of tree  $\mathcal{T}$  is 1 if  $\mu$  is a leaf of  $\mathcal{T}$ , or one plus the sum of the weights of the children of  $\mu$  that are connected through dashed edges. With this biasing, we can implement *expose* and *conceal* with  $O(\log n)$  elementary tree operations. Hence, operation *Change*( $\lambda, x$ ) takes time  $O(\log n)$ , where  $n$  is the size of the tree containing  $\lambda$ .

Operations *MakeVar* and *DeleteVar* can be trivially implemented in  $O(1)$  time. Opera-



**Figure 6:** Example of operation *join*: (a) Solid paths  $\Pi'$  and  $\Pi''$  connected by a dashed edge from the tail of  $\Pi'$  to the head of  $\Pi''$ . (b) Path trees  $\mathcal{B}_{\Pi'}$  and  $\mathcal{B}_{\Pi''}$ . (c) Path tree for the solid path obtained by joining the roots of  $\mathcal{B}_{\Pi'}$  and  $\mathcal{B}_{\Pi''}$ .

tions *Construct*, *Destruct*, *Graft* and *Prune* are performed using variations of the following dynamic tree operations:

- *link*(node  $\rho, \mu$ ) — This operation assumes that  $\rho$  is the root of a tree  $\mathcal{T}$ . Add an edge from  $\rho$  to  $\mu$ , thus making  $\mathcal{T}$  a subtree of the tree containing  $\mu$ .
- *cut*(node  $\mu$ ) — This operation assumes that  $\mu$  is not the root of a tree. Remove the edge from  $\mu$  to its parent, thus separating the subtree rooted at  $\mu$ .

By maintaining solid and dashed edges by using partitioning by weight [23], and representing the path trees as globally biased binary trees [4], we get:

**Theorem 1** *Let  $S$  be a semiring with binary operators  $\oplus$  and  $\otimes$ . There is a fully dynamic data structure for maintaining a collection of expressions over  $S$  with the following performance:*

1. *an expression of size  $n$  uses  $O(n)$  space;*
2. *operations MakeVar and DeleteVar take each  $O(1)$  worst-case time;*
3. *operations Evaluate, Change, Construct, Destruct, Graft and Prune take each  $O(\log n)$  worst-case time, where  $n$  is the total size of the expressions involved in the operation.*

The above theorem can be extended to the case where either or both operations  $\oplus$  and  $\otimes$  admit an inverse. For example, if  $\oslash$  is the inverse of  $\otimes$ , the dependency of the value  $y$  of a node  $\mu$  from the value  $x$  of a descendant of  $\mu$  can be represented by the following canonical form [6]:

$$y = ((a \otimes x) \oplus b) \oslash ((c \otimes x) \oplus d).$$

Hence, we modify the data structure so that the internal nodes of the path trees store a quadruple  $(a, b, c, d)$ . We omit the details on the corresponding modifications to the elementary tree operations, and conclude:

**Corollary 1** *The result of Theorem 1 holds also for expressions that include the inverse of either or both  $\oplus$  and  $\otimes$ .*

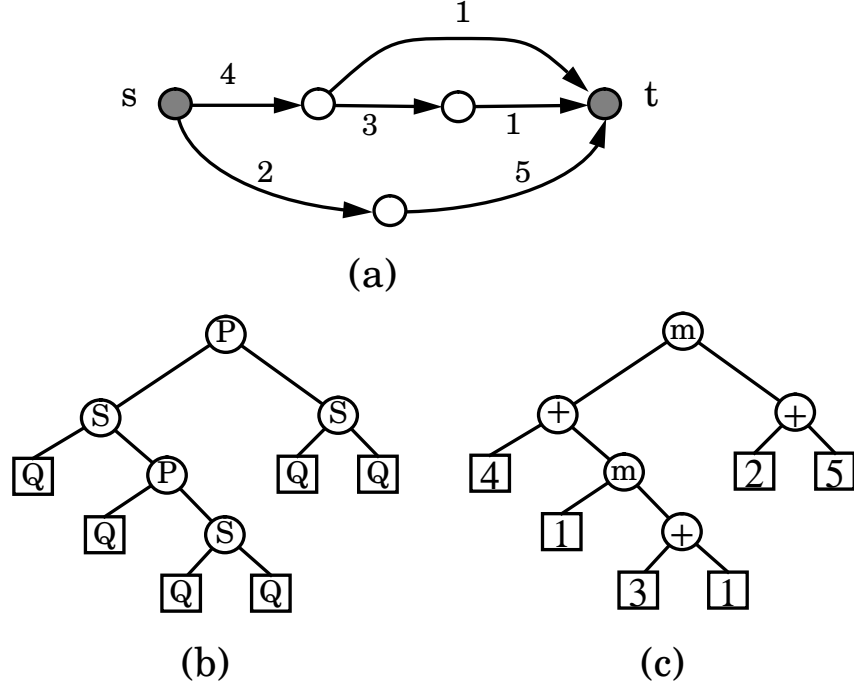
### 3 Series-Parallel Digraphs

An important application of the technique presented in Section 2 is the dynamic maintenance of maximum flow and shortest  $st$ -path in series-parallel digraphs.

A *source* of a digraph  $G$  is a vertex without incoming edges. A *sink* of  $G$  is a vertex without outgoing edges. A *pole* of  $G$  is either a source or a sink of  $G$ . A *series-parallel digraph* is a digraph  $G$  with exactly one source  $s$  and one sink  $t$ , recursively defined as follows (see Fig. 7.a):

- A digraph  $G$  consisting of a single edge from  $s$  to  $t$  is a series-parallel digraph.
- Given series-parallel digraphs  $G'$  and  $G''$  the digraph  $G$  obtained by identifying the source of  $G'$  with the source of  $G''$  and the sink of  $G'$  with the sink of  $G''$  is a series-parallel digraph. This is called a *parallel composition*.
- Given series-parallel digraphs  $G'$  and  $G''$ , the digraph  $G$  obtained by identifying the sink  $t'$  of  $G'$  with the source  $s''$  of  $G''$  is a series-parallel digraph. This is called a *series composition*, and the vertex  $v = s'' = t'$  is called the *join-vertex* of such composition.

A series-parallel digraph is connected, planar and acyclic. It may have multiple edges and can be embedded with the source and sink on the external face. In the following we denote with  $m$  the number of edges of the series-parallel digraph  $G$  currently being considered (the number of vertices is at most  $m + 1$ ), and with  $s$  and  $t$  the source and sink of  $G$ . It is possible to test in  $O(m)$  time whether a digraph is a series-parallel digraph [29]. It has been recently shown that the transitive closure of a series-parallel digraph can be dynamically maintained in  $O(\log m)$  time per query or update operation using  $O(m)$  space [17,27].



**Figure 7:** (a) A series-parallel digraph  $G$  with weighted edges. (b) The SPQ-tree  $T$  of  $G$ . (c) Expression tree for computing the length of a shortest  $st$ -path in  $G$ .

A series-parallel digraph  $G$  is naturally associated with a rooted binary tree  $T$ , which we call *SPQ-tree* of  $G$  (other authors call it *decomposition tree* or *parse tree*). A node  $\mu \in T$  represents a subdigraph of  $G$ , called the *pertinent digraph* of  $\mu$ , and denoted by  $G_\mu$ . If  $\mu$  is a leaf, then  $G_\mu$  is a single edge. Otherwise,  $G_\mu$  is obtained by the composition (series or parallel) of the pertinent digraphs of the children of  $\mu$ . The nodes of  $T$  are of three types: S-nodes, P-nodes, and Q-nodes. Tree  $T$  is defined recursively as follows (see Fig. 7.b):

- If  $G$  is a single edge, then  $T$  consists of a single *Q-node*  $\mu$ .
- If  $G$  is created by the parallel composition of series-parallel digraphs  $G'$  and  $G''$ , let  $T'$  and  $T''$  be the SPQ-trees of  $G'$  and  $G''$ , respectively. The root of  $T$  is a *P-node* and has subtrees  $T'$  and  $T''$ . (The order of the subtrees is arbitrary.)
- If  $G$  is created by the series composition of series-parallel digraphs  $G'$  and  $G''$ , where the sink of  $G'$  is identified with the source of  $G''$ , let  $T'$  and  $T''$  be the SPQ-trees of  $G'$  and  $G''$ , respectively. The root of  $T$  is an *S-node* and has left subtree  $T'$  and right subtree  $T''$ .

The leaves of  $T$  are Q-nodes. The internal nodes are either P-nodes or S-nodes. If  $G$  has  $m$  edges, then  $T$  has  $m$  leaves and hence  $2m - 1$  nodes. Tree  $T$  can be constructed in  $O(m)$  time using the recognition algorithm of [29].

Consider a digraph  $G$  with weighted edges, and let  $s$  and  $t$  be two vertices of  $G$ . Let  $e$  be an edge of  $G$ . For the shortest  $st$ -path problem, the weight of  $e$  represents the length of  $e$ , denoted  $length(e)$ . The shortest  $st$ -path problem consists of finding a directed path in

$$\begin{aligned}
maxflow(\mu) &= \begin{cases} \sum_{\lambda \in children(\mu)} maxflow(\lambda) & \text{if } \mu \text{ is a P-node} \\ \min_{\lambda \in children(\mu)} maxflow(\lambda) & \text{if } \mu \text{ is an S-node} \end{cases} \\
shortest(\mu) &= \begin{cases} \min_{\lambda \in children(\mu)} shortest(\lambda) & \text{if } \mu \text{ is a P-node} \\ \sum_{\lambda \in children(\mu)} shortest(\lambda) & \text{if } \mu \text{ is an S-node} \end{cases}
\end{aligned}$$

**Figure 8:** Equations to calculate the value of  $maxflow(\mu)$  and  $shortest(\mu)$  for  $G_\mu$ , the pertinent digraph for node  $\mu$ .

$G$  from  $s$  to  $t$  of minimum total length. For the maximum flow problem, the weight of  $e$  represents the capacity of  $e$ , denoted  $capacity(e)$ . A flow for  $G$  is a function  $f(e)$  over the edges of  $G$  that satisfies the following conditions:

- for every edge  $e$ ,  $0 \leq f(e) \leq capacity(e)$ ;
- for every vertex  $v \neq s, t$ ,  $\sum_{e \in In(v)} f(e) = \sum_{e \in Out(v)} f(e)$ , where  $In(v)$  and  $Out(v)$  denote the sets of incoming and outgoing edges of  $v$ , respectively.

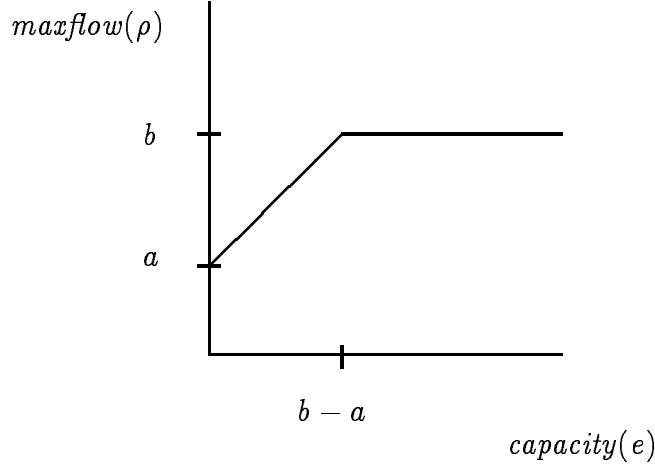
The *value* of  $f$  is given by  $\sum_{e \in Out(s)} f(e) = \sum_{e \in In(t)} f(e)$ . The maximum flow problem consists of finding a flow for  $G$  with maximum value. It is well-known that the maximum flow value is equal to the total capacity of a *minimum cut* separating  $s$  from  $t$  [28].

For a series-parallel digraph  $G$  the problems of determining the value of a maximum flow and the length of a shortest  $st$ -path are closely related. Let  $\mathcal{T}$  be the SPQ-tree of  $G$ . We associate with each node  $\mu$  of  $\mathcal{T}$  the values  $maxflow(\mu)$  and  $shortest(\mu)$ , where  $maxflow(\mu)$  is the maximum flow value of the pertinent digraph  $G_\mu$  of  $\mu$ , and  $shortest(\mu)$  is the length of a shortest  $st$ -path in  $G_\mu$ . (For a Q-node  $\lambda$ ,  $maxflow(\lambda) = capacity(e)$  and  $shortest(\lambda) = length(e)$ , where  $e$  is the edge associated with  $\lambda$ .) Let  $children(\mu)$  denote the set of children of node  $\mu \in \mathcal{T}$ . The resulting equations are shown in Fig. 8.

Therefore, the SPQ-tree of a series-parallel digraph can be viewed as an expression tree with operators  $\min$  and  $+$  over the reals (note that  $+$  distributes over  $\min$ ). For the maximum flow (resp. shortest  $st$ -path) problem, P-nodes store operator  $+$  (resp.  $\min$ ) and S-nodes store operator  $\min$  (resp.  $+$ ). The Q-nodes store the variables of the expression, i.e., the edge weights (see Fig. 7.c).

First, we consider the problem of performing the following operations on a series-parallel digraph  $G$ :

- *MaxFlow(node  $\mu$ )* — Return the maximum flow value for the pertinent digraph  $G_\mu$  of  $\mu$ .
- *ShortestPath(node  $\mu$ )* — Return the length of a shortest path from the source to the sink of the pertinent digraph  $G_\mu$  of  $\mu$ .
- *Update(node  $\lambda$ ; real  $x$ )* — This operation assumes that  $\lambda$  is a Q-node associated with an edge  $e$ . Set the weight of edge  $e$  equal to  $x$ .



**Figure 9:** The maximum flow value for  $G$  (evaluated at the root  $\rho$  of  $\mathcal{T}$ ) versus the capacity of an edge  $e$ ,  $\maxflow(\rho) = \min(a + \text{capacity}(e), b)$ , when  $a < b$ .

By Theorem 1, we have:

**Theorem 2** *Given a series-parallel digraph  $G$  with  $m$  weighted edges, there exists a dynamic data structure that uses  $O(m)$  space and supports operations `MaxFlow`, `ShortestPath`, and `Update` in  $O(\log m)$  worst-case time.*

Let  $\lambda$  be a leaf of the SPQ-tree of  $G$  rooted at  $\rho$ , and  $e$  be the edge associated with  $\lambda$ . The dependence of the value of the maximum flow in  $G$  from the capacity of  $e$  is characterized by a pair  $(a, b)$  and we have:

$$\maxflow(\rho) = \min(a + \maxflow(\lambda), b).$$

If  $b \leq a$ , changes in the capacity of edge  $e$  do not affect the maximum flow of  $G$ . Otherwise,  $b - a$  is the largest contribution of edge  $e$  to the maximum flow, which is achieved by setting  $\text{capacity}(e) = b - a$  (see Fig. 9). Hence, for any flow  $f$  in  $G$  we have  $f(e) \leq b - a$ . Also, edge  $e$  is on some minimum cut if and only if  $\text{capacity}(e) \leq b - a$ . Similar considerations hold for the shortest path problem.

**Corollary 2** *The data structure of Theorem 2 allows to determine whether an edge  $e$  is on some minimum cut or shortest  $st$ -path of  $G$  in  $O(\log m)$  worst-case time. Also, it allows to determine in  $O(\log m)$  time the maximum value of  $f(e)$  over all admissible flows  $f$  in  $G$ .*

Now, we consider a fully dynamic environment for the maintenance of maximum flow and shortest  $st$ -path on a collection  $\mathcal{G}$  of series-parallel digraphs. Namely, in addition to `MaxFlow`, `ShortestPath`, and `Update`, we introduce the following operations:

- *ReportPath*(node  $\mu$ ) — Report a shortest  $st$ -path in the pertinent digraph  $G_\mu$  of node  $\mu$ .
- *ReportCut*(node  $\mu$ ) — Report a minimum cut in the pertinent digraph  $G_\mu$  of node  $\mu$ .
- *MakeDigraph* — Create a new elementary series-parallel digraph  $G$ , represented by a single Q-node with zero weight, and add  $G$  to  $\mathcal{G}$ .

- *DeleteDigraph*(node  $\lambda$ ) — Remove from  $\mathcal{G}$  the elementary series-parallel digraph represented by the single Q-node  $\lambda$ .
- *Compose*(nodetype  $X$ ; node  $\rho', \rho''$ ) — Perform a composition on the series-parallel digraphs  $G_{\rho'}$  and  $G_{\rho''}$ . The composition is series or parallel according to whether  $X = S$  or  $X = P$ . The resulting series-parallel digraph is added to  $\mathcal{G}$  while  $G_{\rho'}$  and  $G_{\rho''}$  are removed from  $\mathcal{G}$ .
- *Attach*(node  $\rho, \lambda$ ) — Replace the edge represented by Q-node  $\lambda$  with the series-parallel digraph  $G_\rho$ . The resulting series-parallel digraph is added to  $\mathcal{G}$  while  $G_\rho$  is removed from  $\mathcal{G}$ .
- *Detach*(node  $\mu$ ) — Remove the pertinent digraph  $G_\mu$  of node  $\mu$  from  $G$  and replace it with a single edge of zero weight. The series-parallel digraph  $G_\mu$  is added to  $\mathcal{G}$ .
- *InsertEdge*(vertex  $v', v''$ ; edge  $e$ ) — Insert a new zero-weight edge  $e$  from  $v'$  to  $v''$ . The operation is performed only if the resulting digraph is a series-parallel digraph.
- *DeleteEdge*(edge  $e$ ) — Delete edge  $e$ . The operation is performed only if the resulting digraph is a series-parallel digraph.
- *InsertVertex*(vertex  $v$ ; edge  $e, e', e''$ ) — Replace edge  $e$  with two zero-weight edges  $e'$  and  $e''$  by inserting vertex  $v$ .
- *DeleteVertex*(node  $\rho$ , vertex  $v$ ; edge  $e, e', e''$ ) — Replace vertex  $v$  and its incident edges  $e'$  (incoming) and  $e''$  (outgoing) with a single zero-weight edge  $e$ . The operation is performed only if  $e'$  and  $e''$  are the only incident edges of  $v$ .

A series-parallel digraph  $G$  can be represented by an SPQ-tree  $\mathcal{T}$  such that if node  $\nu$  is the parent of  $\mu$  and  $\nu$  and  $\mu$  are of the same type, then  $\mu$  is the left child of  $\nu$ . In the following, we will assume that this property, called *chain invariant*, holds, and we refer to the maximal subpaths in  $\mathcal{T}$  of nodes of the same type as *chains*. We call  $\Gamma$  an *S-chain* if it consists of S-nodes and a *P-chain* if it consists of P-nodes. Note that the chain invariant is not restrictive. Any series-parallel digraph can be represented by a SPQ-tree that meets the chain invariant.

Let  $\Gamma = (\mu_1, \mu_2, \dots, \mu_{r-1})$  be an S-chain ordered such that  $\mu_i$  is the left child of  $\mu_{i+1}$  ( $1 \leq i \leq r-2$ ). Note that this implies that the chain subpaths run from the leaves toward the root of the tree. The *skeleton* of  $\Gamma$ , denoted  $\text{skeleton}(\Gamma)$ , is the digraph consisting of  $r$  edges  $e_i = (v_{i-1}, v_i)$ ,  $1 \leq i \leq r$ , where  $v_0$  and  $v_1$  are the source and sink of the pertinent digraph of the left child of  $\mu_1$ , and  $v_i$ ,  $2 \leq i \leq r$ , is the sink of the pertinent digraph of  $\mu_{i-1}$ . For  $i = 1, \dots, r-1$ , node  $\mu_i$  is called the *proper node* of vertex  $v_i$ . Note that  $v_i$  is the join-vertex used in the series composition at node  $\mu_i$ . Vertices are implicitly represented by the SPQ-tree. Each vertex distinct from  $s$  and  $t$  is mapped to its unique proper node. Each node is mapped to the source and sink of its pertinent digraph. Additionally, each S-node is the proper node of a unique vertex. The concepts of proper node and skeleton are analogous to the corresponding ones developed for planar *st*-graphs in [10].

We equip the tree  $\mathcal{T}$  with a secondary data structure for maintaining the S-chains and their skeletons. Namely, we store with each S-chain  $\Gamma = (\mu_1, \mu_2, \dots, \mu_{r-1})$  the poles  $v_0$  and  $v_r$  of its skeleton and a balanced binary tree, called *routing tree*. The  $i$ -th ( $1 < i < r$ ) leaf of the routing tree of  $\Gamma$  represents edge  $e_i$  of  $\text{skeleton}(\Gamma)$  and the  $i$ -th internal node ( $1 < i < r-1$ ) represents the join vertex of node  $\mu_i$ . The routing trees allows us to find in  $O(\log m)$  time

the proper S-node of a vertex  $v$  and the first and last nodes of the S-chain containing a node  $\mu$ . A similar technique of augmenting dynamic trees with balanced trees is described in [11].

Operations *MakeDigraph* and *DeleteDigraph* can be trivially implemented in  $O(1)$  time. Operations *Attach* and *Detach* correspond to *Graft* and *Prune*, respectively, and hence take  $O(\log m)$  time. We perform operation *Compose*( $X, \rho', \rho''$ ), with  $X = S$  or  $X = P$ , as follows. If  $\rho''$  is not an X-node, we do a *Construct* operation with the appropriate operator. Otherwise, we need to maintain the chain invariant. Using the routing tree of the chain  $\Gamma''$  containing  $\rho''$  we access the first node of  $\Gamma''$  and prune with a *Detach* operation its left subtree, which is replaced by a leaf  $\lambda$ . Then, we perform a *Construct* operation on  $\rho'$  and the pruned subtree. Finally, we graft the resulting tree to  $\lambda$  with an *Attach* operation (for example, see figure 10. Hence, *Compose* takes  $O(\log m)$  time.

Consider now operation *InsertVertex*( $v, e, e', e''$ ), and let  $\lambda$  be the Q-node of  $e$ . If  $\lambda$  is a left child or its parent is a P-node, we simply replace  $\lambda$  with an S-node having Q-node children for  $e'$  and  $e''$ . This can be done in  $O(\log m)$  time with two *MakeDigraph* operations, followed by a *Compose* operation and an *Attach* operation. Otherwise ( $\lambda$  is a right child of an S-node  $\mu$ ) we perform the following sequence of operation: (a) *Detach*( $\nu$ ), where  $\nu = \text{sib}(\lambda)$ ; (b) create new Q-nodes  $\lambda'$  and  $\lambda''$  for  $e'$  and  $e''$  using *MakeDigraph*; (c) *Attach*(*Compose*( $S, \nu, \lambda'$ ),  $\text{sib}(\lambda)$ ); and (d) *Attach*( $\lambda'', \lambda$ ). All this takes  $O(\log m)$  time.

The inverse operation *DeleteVertex*( $e, v, e', e''$ ) is implemented similarly with a constant number of *MakeDigraph*, *DeleteDigraph*, *Attach* and *Detach* operations.

The restructuring of the SPQ-tree caused by *InsertEdge* and *DeleteEdge* is more complex. The following lemmas determine when it is possible to perform such operations.

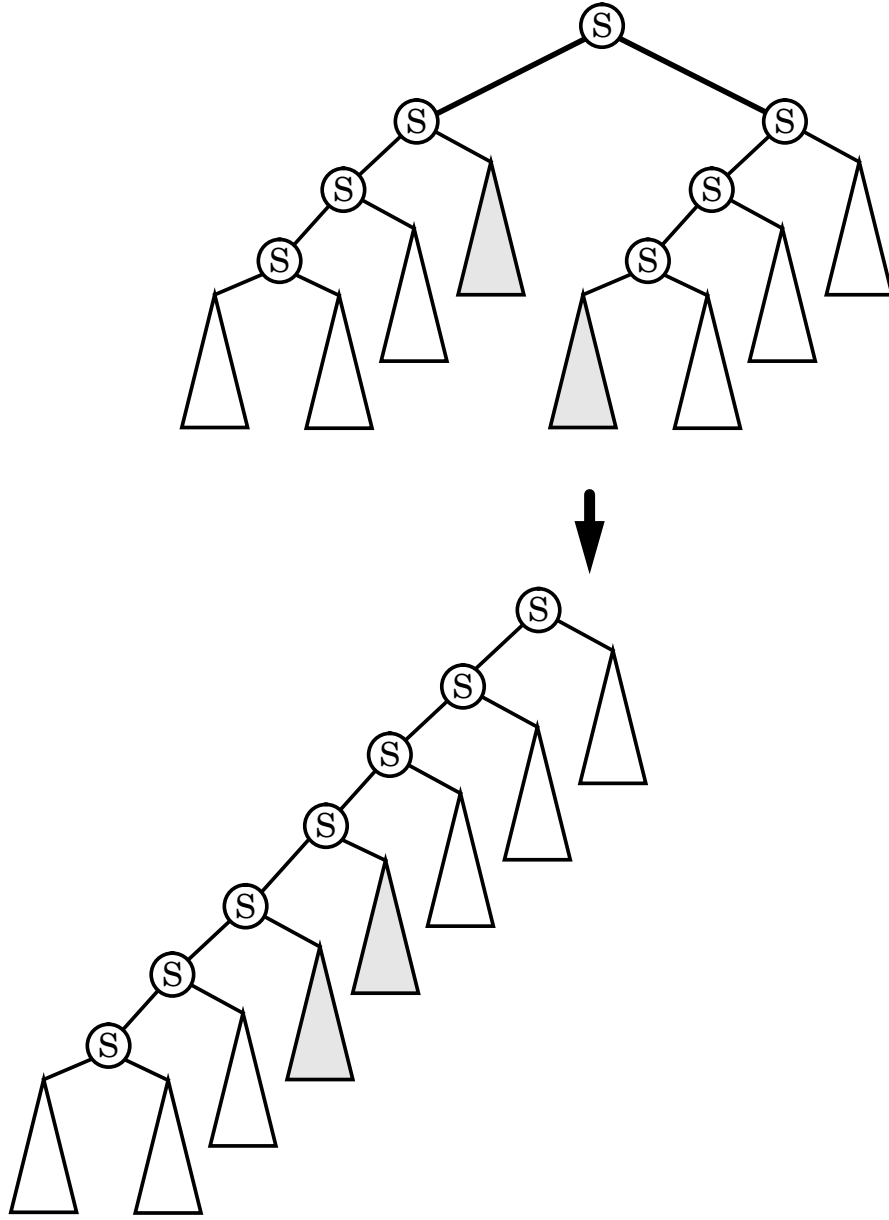
**Lemma 1** *Let  $G$  be a series-parallel digraph with SPQ-tree  $T$ , and  $v'$  and  $v''$  be two vertices of  $G$ . Then the digraph  $\tilde{G}$  obtained by inserting an edge from  $v'$  to  $v''$  is a series-parallel digraph if and only if one of the following conditions is true:*

1.  $v' = s$  and  $v'' = t$ , or
2.  $v'$  and  $v''$  are vertices of the skeleton of the same S-chain of  $T$ , with  $v'$  preceding  $v''$ .

**Proof:**

(If) If  $v'$  and  $v''$  meet either condition in the lemma, then  $\tilde{G}$  is clearly a series-parallel digraph with an additional parallel composition.

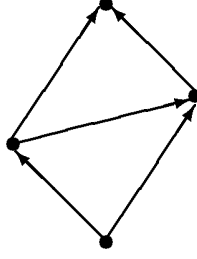
(Only If) We use the property that a series-parallel digraph has no subgraph homeomorphic to the digraph  $W$  shown in Fig. 11 [29]. Suppose that  $v'$  and  $v''$  do not meet either condition of the lemma. Assume that  $T$  keeps the chain invariant. Let  $\mu$  be the least common ancestor in  $T$  of the proper nodes of  $v'$  and  $v''$ , and  $\Gamma$  be the chain containing  $\mu$ . Let  $e$  be the edge from  $v'$  to  $v''$ . If  $\mu$  is a P-node then we consider the subgraph of  $\tilde{G}$  consisting of edge  $e$  and two directed paths from the poles of  $G_\mu$  through the pertinent digraphs of the children of  $\mu$ . Such a digraph is homeomorphic to  $W$ , which implies that  $\tilde{G}$  is not a series-parallel digraph. Now, suppose that  $\mu$  is an S-node;  $G$  has a directed path either from  $v'$  to  $v''$  or from  $v''$  to  $v'$ . If  $G$  has a path from  $v''$  to  $v'$ , then  $\tilde{G}$  has a cycle and hence is not a series-parallel digraph. Thus,  $G$  has a path from  $v'$  to  $v''$ . Since Condition 2 is not met, at least one of  $v'$  and  $v''$ , say  $v''$ , is not a vertex of  $\text{skeleton}(\Gamma)$ , so that the pertinent digraph of a child of  $\mu$  contains  $v''$  and has a directed path  $p$  between its poles that avoids  $v''$ . Thus, we can identify in  $\tilde{G}$  a subgraph homeomorphic to  $W$  that consists of edge  $e$ , path  $p$ , and a path from  $v'$  to the sink of  $G_\mu$  through  $v''$ , so that  $\tilde{G}$  is not a series-parallel digraph.  $\square$



**Figure 10:** An example of restoring the chain invariant after a composition.

**Lemma 2** *Let  $G$  be a series-parallel digraph with SPQ-tree  $T$ , and  $v'$  and  $v''$  be two vertices of  $G$ . Let  $\Gamma'$  and  $\Gamma''$  be the  $S$ -chains containing the proper nodes  $\mu'$  of  $v'$  and  $\mu''$  of  $v''$ , respectively, and  $\Gamma$  be the  $S$ -chain containing the least-common ancestor of  $\mu'$  and  $\mu''$ . Then the digraph obtained by inserting an edge from  $v'$  to  $v''$  is a series-parallel digraph if and only if one of the following conditions is true:*

1.  $v' = s$  and  $v'' = t$ ,
2.  $\Gamma = \Gamma' = \Gamma''$  and  $v'$  precedes  $v''$  in  $\text{skeleton}(\Gamma)$ ,
3.  $\Gamma = \Gamma'$  and  $v'$  is the source of  $\text{skeleton}(\Gamma'')$ , or



**Figure 11:** Digraph  $W$  that is a forbidden homeomorphic subgraph for a series-parallel digraph.

4.  $\Gamma = \Gamma''$  and  $v''$  is the sink of  $\text{skeleton}(\Gamma')$ .

**Proof:** Follows directly from Lemma 1. □

The following lemma determines when we can delete an edge.

**Lemma 3** *Let  $G$  be a series-parallel digraph  $G$  with SPQ-tree  $\mathcal{T}$ , and  $e$  be an edge of  $G$  represented by  $Q$ -node  $\lambda$ . The digraph obtained by removing edge  $e$  is a series-parallel digraph if and only if one of the following conditions is true:*

1. the parent of  $\lambda$  in  $\mathcal{T}$  is a  $P$ -node,
2.  $e$  is the only outgoing edge of  $s$ , or
3.  $e$  is the only incoming edge of  $t$ .

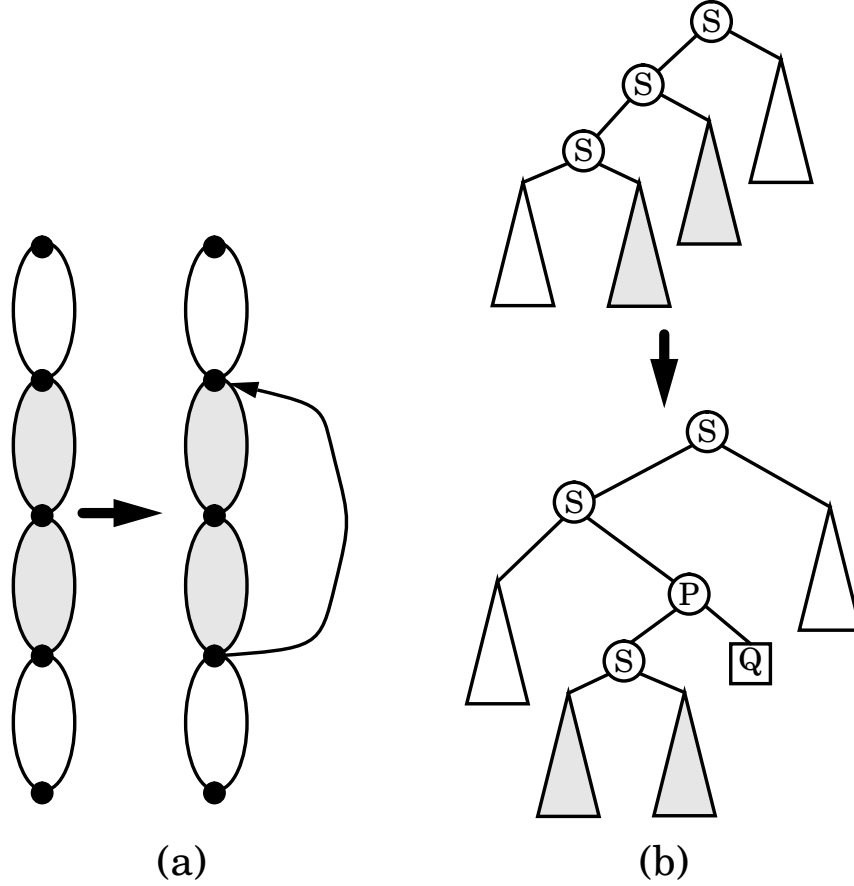
**Proof:** If the parent of  $\lambda$  in  $\mathcal{T}$  is a  $P$ -node, then  $G'$  is the series-parallel digraph that results by omitting the parallel composition step on  $e$  in the construction of  $G$ . If the parent of  $\lambda$  is an  $S$ -node, then removing  $e$  adds a second source or disconnects  $G'$  unless  $e$  is the only edge originating at  $s$  or  $e$  is the only edge terminating at  $t$ . □

Testing for the applicability of  $\text{InsertEdge}(v', v'')$  is performed as follows: (a) find the proper nodes of  $v'$  and  $v''$  using the routing trees (this takes  $O(\log m)$  time); (b) test for the conditions in Lemma 2. Condition 2 can be checked in  $O(\log m)$  time using the routing tree of  $\Gamma$ . Conditions 1, 3, and 4, can be checked in  $O(1)$  time. Hence, the total time is  $O(\log m)$ .

Operation  $\text{InsertEdge}(v', v'', e)$  is done as follows (see Figs. 12–13). Find the proper nodes of  $v'$  and  $v''$  and their corresponding chains  $\Gamma'$  and  $\Gamma''$ . Exit if none of the conditions in Lemma 2 is satisfied. Without loss of generality, assume that  $\Gamma''$  is a descendant of  $\Gamma'$  or  $\Gamma' = \Gamma''$ . Using the routing tree of  $\Gamma''$ , determine the nodes of  $\Gamma''$  associated with the subchain from  $v'$  to  $v''$ . Finally, perform the appropriate transformation from Fig. 12 or Fig. 13. This can be done with a constant number of *MakeDigraph*, *Compose*, *Attach*, and *Detach* operations. Therefore,  $\text{InsertEdge}$  takes  $O(\log m)$  time.

After testing the conditions of Lemma 3, operation  $\text{DeleteEdge}(e)$  is done with a constant number of *DeleteDigraph*, *Attach*, and *Detach* operations. Therefore, also  $\text{DeleteEdge}(e)$  takes  $O(\log m)$  time.

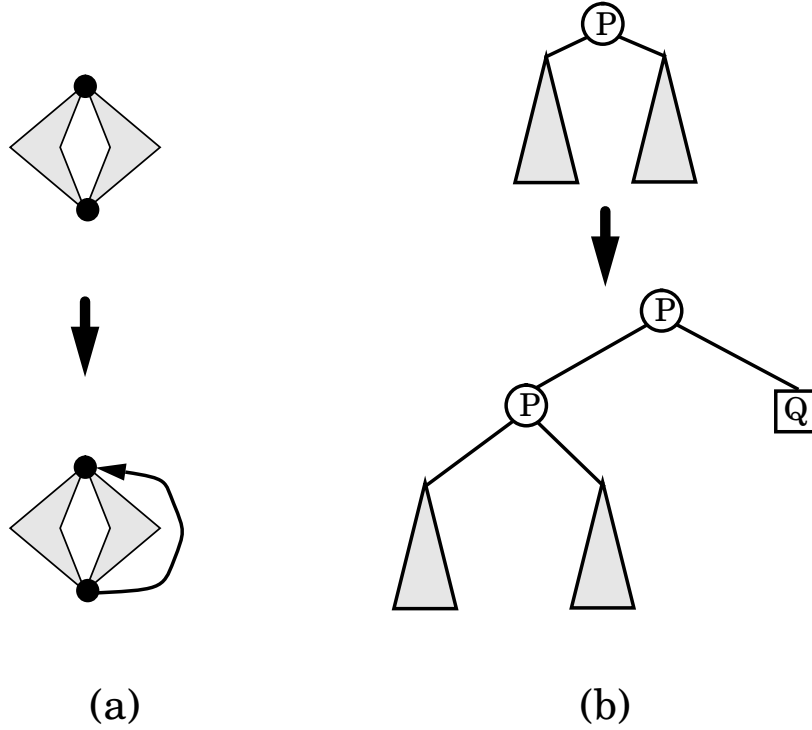
Now, we show how to perform operation  $\text{ReportPath}(\mu)$ . A similar technique can be used



**Figure 12:** Modification of  $\mathcal{T}$  in operation *InsertEdge*: splitting an S-chain. (a) Insertion of an edge between nonconsecutive vertices of the skeleton of an S-chain. (b) Transformation of the SPQ-tree.

for  $\text{ReportCut}(\mu)$ . If  $\mu$  is a leaf, we report the corresponding edge and exit. If  $\mu$  is an S-node, a shortest path in  $G_\mu$  must go through the pertinent digraphs of both children of  $\mu$ . Hence, we call recursively *ReportPath* on the left and right child of  $\mu$ . If  $\mu$  is a P-node, we consider the P-chain  $\Gamma$  of  $\mu$  and we denote with  $\Delta_\mu$  the set of all nodes  $\nu$  such that  $\nu$  is a child of a node of  $\Gamma$  but  $\nu$  is not on  $\Gamma$ . A shortest path in  $\mu$  goes through the pertinent digraph  $G_{\nu'}$  of a node  $\nu' \in \Delta_\mu$  such that  $\text{shortest}(\nu')$  is minimum. Hence, we call recursively *ReportPath* on such node  $\nu'$ .

To determine quickly node  $\nu'$  we modify the path trees such that each internal node  $\eta$  of a path tree  $\mathcal{B}_\Pi$  stores a pointer to a node  $\kappa$  of the SPQ-tree that has the minimum value of  $\text{shortest}$  over all nodes that are children of nodes in the subpath of  $\Pi$  from  $\text{head}(\eta)$  to  $\text{tail}(\eta)$  but are not on such subpath. Since these nodes are tails of solid paths, their  $\text{shortest}$  values are actual. Also, we set-up routing trees associated with the P-chains. With these additional structures we find node  $\nu'$  in time  $O(\log m)$  as follows: (a) determine the bottommost node  $\mu'$  of the P-chain  $\Gamma$  of  $\mu$  using the routing tree of  $\Gamma$ ; (b) create a solid path  $\Pi$  from  $\mu'$  to  $\mu$  by calling  $\text{expose}(\mu')$  followed by  $\text{expose}(\mu'')$ , where  $\mu''$  is the parent of  $\mu$ ; (c) find  $\nu'$  by following the pointer stored at the root of  $\mathcal{B}_\Pi$ ; (d) restore the original solid paths and dashed



**Figure 13:** Modification of  $\mathcal{T}$  in operation *InsertEdge*: extending a P-chain. (a) Insertion of an edge between consecutive vertices of the skeleton of an S-chain (the vertices are the source and sink of a parallel composition). (b) Transformation of the SPQ-tree.

edges with two *conceal* operations.

We summarize with the following theorem:

**Theorem 3** *There is a fully dynamic data structure for maintaining a collection of edge-weighted series-parallel digraphs with the following performance:*

1. *a series-parallel digraph with  $m$  edge uses  $O(m)$  space;*
2. *operations MakeDigraph and DeleteDigraph take each  $O(1)$  worst-case time;*
3. *operations MaxFlow, ShortestPath, Update, Compose, Attach, Detach, InsertEdge, DeleteEdge, InsertVertex, and DeleteVertex take each  $O(\log m)$  worst-case time, where  $m$  is the total number of edges of the series-parallel digraphs involved;*
4. *operations ReportCut and ReportPath take each  $O(k \log m)$  worst-case time, where  $m$  is the number of edges of the series-parallel digraph involved, and  $k$  is the number of edges of the cut or path being reported.*

## 4 Further Applications

Complex geometric solids are represented in Constructive Solid Geometry (CSG) as a hierarchic combination of primitive objects (e.g., spheres and tetrahedra) using set operations such as union, intersection, and difference. The resulting compound object is described by

a CSG-tree whose leaves are the primitive objects and whose internal nodes are set operators. CSG representations are a fundamental tool in computer graphics. Several efficient algorithms are given in [15] for CSG classification problems, such as determining whether a query point is inside a compound object.

We study a dynamic point inclusion problem where a collection of compound objects is modified by changing their primitive constituents and by update operations, such as link and cut, on their CSG-trees. Queries consist of reporting whether a given compound object contains a fixed point  $p$ . By applying dynamic expression trees to the technique of [15], we obtain:

**Theorem 4** *The above dynamic point inclusion problem on a collection of compound objects defined by CSG-trees of total size  $n$  can be solved using an  $O(n)$ -space data structure that supports query and update operations in  $O(\log n)$  worst-case time.*

Dynamic expression trees have immediate application to the problem of compacting *slicing floorplans*, a layout technique widely used in VLSI (see, e.g., [25]). A slicing floorplan is either a rectangle (called basic rectangle), or is the union of two slicing floorplans that share a horizontal side (called horizontal slice) or a vertical side (called vertical slice). Each basic rectangle  $r$  has a minimum width  $w_r$  and a minimum height  $h_r$ , which describe the dimensions of a circuit module to be placed inside  $r$ .

An important problem in VLSI layout is the minimization of the area of the slicing floorplan, subject to the above constraints on the height and width of the basic rectangles. This problem can be solved sequentially in  $O(n)$  time,  $n$  being the number of basic rectangles, by representing the floorplan with two expression-trees,  $\mathcal{T}_w$  and  $\mathcal{T}_h$ , where  $\mathcal{T}_w$  is used to find the minimum width and  $\mathcal{T}_h$  is used to find the minimum height [25]. Each leaf of  $\mathcal{T}_w$  represents a basic rectangle  $r$  and stores  $w_r$ . Each internal node of  $\mathcal{T}_w$  represents a slice and stores operator “+” for a vertical slice, and operator “max” for a horizontal slice. Hence, the minimum width of the slicing floorplan is the value at the root of  $\mathcal{T}_w$ . Analogous considerations can be done for tree  $\mathcal{T}_h$ .

By implementing trees  $\mathcal{T}_w$  and  $\mathcal{T}_h$  with dynamic expression trees, we can solve a dynamic version of the compaction problem for slicing floorplans, where query operations ask for the minimum area of a floorplan (or sub-floorplan) and update operations change the dimensions of a basic rectangle or join (or separate) two floorplans.

**Theorem 5** *There exists a fully dynamic  $O(n)$ -space data structure for maintaining a collection of slicing floorplans of total size  $n$ , such that a query or update operation takes  $O(\log n)$  worst-case time.*

## 5 Open Problems

We conclude the paper with the following open problems:

- Modify the data structure for series-parallel digraphs so that a shortest path or minimum cut can be reported in time  $O(\log n + k)$ .
- Develop an efficient technique for the dynamic maintenance of the shortest  $st$ -path or maximum flow in planar digraphs. Our technique allows to solve the problem for

planar  $st$ -graphs with  $O(r \log n)$  query and update time, where  $n$  is the number of vertices and  $r$  is the size of the largest triconnected component of the graph. A planar  $st$ -graph is a planar acyclic digraph with one source and one sink, both on the external face. Planar  $st$ -graphs include series-parallel digraphs as a special case. No algorithm with polylogarithmic query and update times is known even for planar  $st$ -graphs with unit edge weights.

- Develop an efficient technique for the dynamic maintenance of the output value(s) of an arithmetic or boolean circuit. For general circuits it is unlikely that an algorithm with polylogarithmic query and update times exists, since the related problem of parallel evaluation of circuits is P-complete [18]. However, efficient dynamic solutions may exist for special classes of circuits that include trees.

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