Definition: The following is an OR circuit.

Figure 1: OR circuit

Definition: The following is an AND circuit.

Figure 2: AND circuit
Definition: The following is a NOT circuit.

![NOT circuit](image1)

Figure 3: NOT circuit

Definition: A combinatorial circuit is a circuit with AND, OR, and NOT gates.

Definition: A Half Adder is a circuit that computes the sum of two bits along with the carry bit.